

Qualification of ASE Chungli as an Alternate Assembly Site for MSOP Devices

MSOP at ASE Chungli (ASE-AET) Qualification

QUALIFICATION RESULT			
TEST	SPECIFICATION	SAMPLE SIZE	RESULT
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3 x 77	PASS
Highly Accelerated Stress Test (HAST)*	JEDEC <i>JESD22-A110</i>	3 x 77	PASS
Autoclave (AC)*	JEDEC <i>JESD22-A102</i>	3 x 77	PASS
Unbiased Highly Accelerated Stress Test (UHAST)*	JEDEC <i>JESD22-A118</i>	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3 x 11	PASS
High Temperature Storage (HTS)	JEDEC <i>JESD22-A103</i>	1 x 77	PASS
Electrostatic Discharge <i>Field Induced Charge Device Model</i>	JEDEC <i>JESD22-C101</i>	3/voltage	PASS ±1250V

*Preconditioned per JEDEC/IPC J-STD-020

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Automotive Qualification Results Summary of MSOP Package at ASE Chungli (ASE-AET)

QUALIFICATION RESULT			
TEST	SPECIFICATION	SAMPLE SIZE	RESULT
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3 x 77	PASS
Highly Accelerated Stress Test (HAST)*	JEDEC <i>JESD22-A110</i>	3 x 77	PASS
Unbiased Highly Accelerated Stress Test (UHAST)*	JEDEC <i>JESD22-A118</i>	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3 x 11	PASS
High Temperature Storage (HTS)	JEDEC <i>JESD22-A103</i>	3 x 77	PASS
Electrostatic Discharge <i>Field Induced Charge Device Model</i>	JEDEC <i>JESD22-C101</i>	3/voltage	PASS ±1250V

* These samples were subjected to preconditioning (per J-STD-020 Level 1) prior to the start of the stress test. Level 1 preconditioning consists of the following: Bake: 24 hrs @ 125°C, Unbiased Soak: 168 hrs @ 85°C, 85%RH, Reflow: 3 passes through an oven with a peak temperature of 260°C. TC samples were subjected to wire-pull test after 500 cycles with results within specification limits